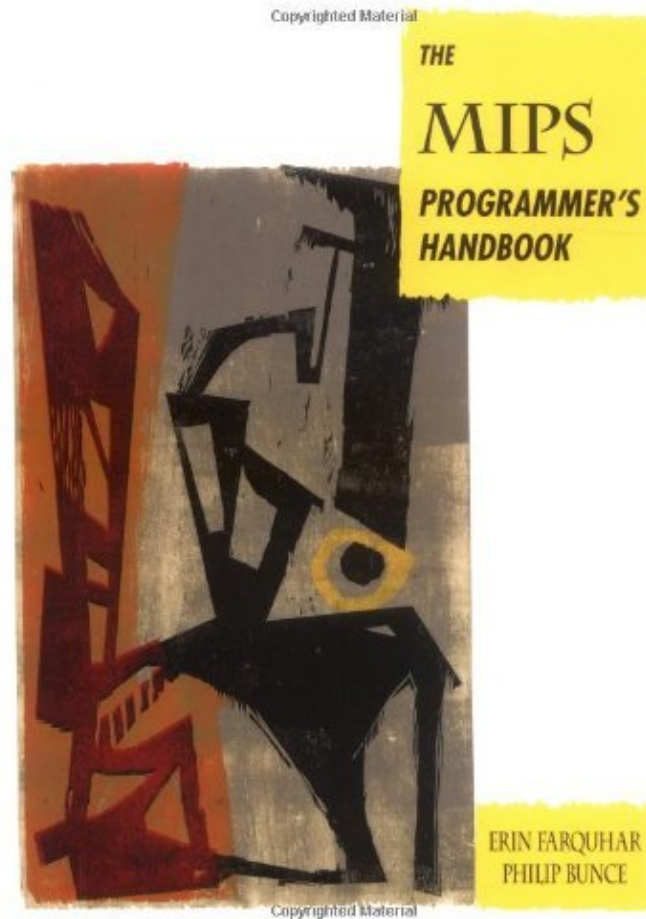


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The MIPS Programmer's Handbook (The Morgan Kaufmann Series in Computer Architecture and Design)

Von Erin Farquhar, Philip J. Bunce
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Von Erin Farquhar, Philip J. Bunce : **The MIPS Programmer's Handbook (The Morgan Kaufmann Series in Computer Architecture and Design)** before purchasing it in order to gage whether or not it would be worth my time, and all praised The MIPS Programmer's Handbook (The Morgan Kaufmann Series in Computer Architecture and Design):

KundenrezensionenHilfreichste Kundenrezensionen1 von 1 Kunden fanden die folgende Rezension hilfreich. Good book for computer science students!Von Don MarcoThis book offers an overview of the assembler language, designed for the family of mips processors r2000 and r3000 (nothing about r4000 and r6000 series). It also shows common C-functions translated in assembler. It explains, in general guidelines, the internal architecture of the r2000 processor

thus not as deep as D. Patterson's books. Beyond this, the text is a bit confused, the chapters don't lead the reader to any meaningful point, offering pieces of explanations thought for an already instructed reader. In fact a beginner won't find here a good introduction to the subject. E.g. at the very beginning, when the author deals with the structure of the registers (with many long, meaningless words without any explanation), does anybody know what a 'C0_BADVA' is? After that it goes along, is a single page, to the stack and the initialization of the eproms. If, on the other side, the reader already knows the subject, this book is a good and useful guide when he needs source codes and hints for his code writing. If someone wonders where a mips processor can be found... Uhm.. In a Playstation or in a handheld. Well this text is particularly suitable for courses dealing with computer science. Last but not least the price. In my humble opinion it is quite high for a 350 page-long book.

Kurzbeschreibung A hands-on view of the highly successful MIPS family of microprocessors, written for programmers developing systems applications for the MIPS platform. The MIPS Programmer's Handbook describes the MIPS architecture from the perspective of assembly- and C-language programmers, with special emphasis on issues related to embedded applications. Engineers writing system-level programs for MIPS-based embedded systems will find the topic selection especially useful including the sections on software conventions, initializing the processor in a bare machine environment, and writing exception handlers. For convenient use, the instruction set reference is presented with only one page per instruction. The authors focus on the instructions available to assembly-language programmers, rather than on the hardware-level instruction set documented in data books released by vendors of the MIPS processor. Provides enough detail for anyone doing serious system-level programming. Also included are ten complete program examples, with line-by-line explanations.

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