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## See MIPS Run (The Morgan Kaufmann Series in Computer Architecture and Design)

Von Dominic Sweetman

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# See MIPS Run



Dominic Sweetman

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**Von Dominic Sweetman : See MIPS Run (The Morgan Kaufmann Series in Computer Architecture and Design)** before purchasing it in order to gauge whether or not it would be worth my time, and all praised See MIPS Run (The Morgan Kaufmann Series in Computer Architecture and Design):

Kundenrezensionen Hilfreichste Kundenrezensionen 2 von 2 Kunden fanden die folgende Rezension hilfreich. Good but not practical Von KRI like the content. I hardly can use the book as a reference because it is missing a convenient section with mnemonics and their effects like you can find on other programming manual (I love the PowerPC one from IBM.) The succinct part where the mnemonics are given is too small and not complete. Therefore I have to rely on some other manuals to understand precisely the output of the processor functions on its various registers. The book gives a good insight at MIPS architecture including VM and examples and pretty much everything is covered here. I

wish the examples would have an extra column with the hexa values of there instructions counterpart to give some info on the way instructions are built.1 von 2 Kunden fanden die folgende Rezension hilfreich. A great book with lots of detail.Von Ein KundeThe best book on programming a MIPS processor I have ever encountered. Lots of good tidbits and stuff.

**Kurzbeschreibung**The versatile offspring of an extended family of multiple chip companies, today's MIPS chips are everywhere. They power everything from videogames, network routers, laser printers, set-top boxes, and high-performance workstations. This book brings together this extraordinary proliferation of form and functionality, offering embedded systems programmers and designers unique, eminently practical insights into MIPS. It covers how MIPS started, the principles at the root of the RISC revolution, the full details of the MIPS instruction set, and how these details together constitute a full operating system ready to be put to work in hundreds of ways. If you're programming embedded systems and need to understand the chips at the deepest level, or even if you're just curious, you're sure to find what you need in this book. It's all here, from the nuts and bolts of a programming reference to the big picture that only a true expert can deliver. So buy the book. Take it home. Step inside. And see MIPS run.\*  
Written by an independent consultant whose business is understanding MIPS architecture and embedded systems programming.\* Addresses the evolution of MIPS technology, giving you a solid foundation for successful designs and implementations.\* Provides an in-depth, easy-to-use guide to the MIPS instruction set, including special attention to processor control and assembler mnemonics for every instruction.\* Covers everything from MIPS I to MIPS IV, with appendices devoted to the optional MIPS 16 instruction set and V/MDMX.

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